Volume 10, Issue 5, May – 2025 International Journal of Innovative Science and Research Technology
ISSN No:-2456-2165 https://doi.org/10.38124/ijisrt/25may692

# Evolution of Embedded Computing: A Technological Journey from General Purpose Processors (GPP) to System on Chip (SoC)

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Publication Date: 2025/05/20

**Abstract: The evolution of embedded computing reflects the technological advancements that have transitioned from General Purpose Processors (GPPs) to highly integrated System on Chip (SoC) architectures. This paper investigates the transformation through intermediary technologies, including Microcontroller Units (MCUs) and Circuit on Board (COB), highlighting how each innovation addressed the limitations of its predecessors. The study outlines the fundamental architecture, functionalities, advantages, and real-world applications of each stage, emphasizing their role in the development of modern embedded systems. Through a comparative lens, this research provides insight into how integration, miniaturization, power efficiency, and performance optimization have shaped current and future computing solutions.**

**How to Cite:** Ashutosh Mishra (2025) Evolution of Embedded Computing: A Technological Journey from General Purpose Processors (GPP) to System on Chip (SoC). *International Journal of Innovative Science and Research Technology,* 10(5), 631-633. https://doi.org/10.38124/ijisrt/25may692

## I. INTRODUCTION

Computing technology has seen a significant transition in its design and implementation approach over the last few decades. What began with General Purpose Processors (GPPs) has matured into complex and compact systems known as System on Chip (SoC). These advances have supported the growth of consumer electronics, industrial automation, medical systems, and communication infrastructures. This paper explores this journey by examining the milestones in the evolution of embedded computing devices.

## II. GENERAL PURPOSE PROCESSORS (GPP)

### *A. Instruction Cycle: Fetch, Decode, Execute*

GPPs follow a systematic instruction cycle comprising three fundamental phases: Fetch, Decode, and Execute. The program counter (PC) guides the processor to fetch instructions from memory. These instructions are decoded to determine the operation and operands, which are then executed by the processor.

### *B. Evolution of Clock Speeds*

The initial microprocessors operated at clock speeds as low as 4 MHz. With the advancement in semiconductor technology and miniaturization, modern processors now operate at speeds exceeding 5 GHz. Overclocking techniques allow even higher performance, pushing the boundaries of processing speed.

### *C. Caches and Pipelining*

GPPs incorporate cache memory to bridge the speed gap between the CPU and RAM. Pipelining allows multiple instruction phases to be processed concurrently, improving throughput and efficiency.

### *D. Multicore Architectures*

Modern processors often feature multicore designs, where multiple cores handle different tasks or threads simultaneously. Technologies like hyper-threading further enhance performance by allowing a single core to manage multiple threads.

## III. MICROCONTROLLER UNITS (MCUS)

### *A. Overview and Architecture*

Microcontrollers integrate a CPU, memory, I/O ports, and peripherals into a single chip. This compact design makes them ideal for dedicated control tasks in embedded systems.

### *B. Applications*

MCUs are widely used in consumer electronics (washing machines, printers), automotive systems (ABS, EPS), and industrial automation (motor control systems).

### *C. Evolution of Memory*

The introduction of EEPROM and Flash memory revolutionized MCUs, enabling in-system programming and firmware updates without removing the chip.

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*D. Classification by Bit Width*

- 4-bit: Basic applications like electronic toys.
- 8-bit: Widely used in control systems; Intel 8051, PIC16X.
- 16-bit: High-speed control (e.g., robotics).
- 32-bit: Complex applications (e.g., image processing).

*E. Key Features*

- RAM, ROM, I/O lines
- Timers, ADC/DAC
- Serial communication: UART, SPI, I²C
- Watchdog timers and PWM modules

*F. MCU Families*

- Intel 8051 series
- Microchip PIC series
- Atmel AVR series
- Freescale 68HC11
- STMicro STM32

## IV. CIRCUIT ON BOARD (COB)

*A. Definition*

COB involves mounting individual components like CPUs, RAM, and peripherals onto a printed circuit board (PCB).

*B. Limitations*

- Larger physical size
- Higher power consumption
- Complex design and interconnections

*C. Transitional Importance*

COB systems allowed for modular design and were key in transitioning from microcontroller-based designs to more integrated SoC platforms.

## V. SYSTEM ON CHIP (SOC)

*A. Architecture and Integration*

SoCs integrate all essential components, including CPU, GPU, RAM, ROM, ADCs/DACs, timers, and communication interfaces, onto a single silicon chip.

*B. Design Flow*

- Architectural design
- Hardware-software co-design
- Simulation and testing
- Semiconductor fabrication

*C. Core Components*

- Multi-core processors
- Memory modules (cache, SRAM, Flash)
- Communication interfaces (I²C, SPI, UART, CAN)
- Analog/RF circuits
- Custom hardware for specific applications

*D. Network on Chip (NoC)*

NoC enhances internal communication among SoC components using packet-switched networks for improved scalability and bandwidth.

*E. Applications*

- Smartphones, tablets
- Automotive ECUs
- Industrial robots
- Smart TVs and wearables

## VI. COMPARATIVE ANALYSIS

Table 1 Comparative Analysis

| Feature | GPP | MCU | COB | SoC |
|---|---|---|---|---|
| Integration Level | Low | Medium | Medium | High |
| Performance | High | Moderate | Variable | Very High |
| Power Consumption | High | Low | Medium | Very Low |
| Size | Large | Small | Medium | Very Small |
| Cost | High | Low | Medium | Low |
| Real-time Support | Limited | High | Limited | High |
| Applications | General | Embedded | Modular | Versatile |

## VII. CONCLUSION

The transition from General Purpose Processors to System on Chip represents a paradigm shift in the design and deployment of embedded computing systems. GPPs provided flexibility, but their size and power consumption limited their embedded use. MCUs enabled compact control systems, while COB allowed for modular integration. The advent of SoCs has revolutionized embedded system design by integrating all components into a single chip, enhancing performance and reducing cost and power consumption. The continued evolution of SoC technologies promises even more powerful and energy-efficient systems for the future.

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